

Amendments to the Specification:

Please replace paragraph [0005] on page 2 with the following amended paragraph:

[0005] Fig. 1 is a block diagram of a simplified cellular telephone system that may be used to implement the present invention. Remote stations such as remote stations 10 (typically cellular telephones) are located around base stations 12. The remote stations 10a and 10b are in an active mode and are therefore interfacing with one or more base stations 12 using radio frequency (RF) signals modulated in accordance with the CDMA signal processing techniques of the cdma2000 standard. A system and method for modulating RF signals in accordance with CDMA modulation is described in U.S. Patent No. 5,103,459 entitled "System and Method for Generating Signal Waveforms in a CDMA Cellular Telephone System" assigned to the assignee of the present invention and incorporated herein by reference. The other remote stations 10 are in standby mode and are therefore monitoring either a full paging channel for page messages indicating a request to communicate, or they are monitoring a quick paging channel for indicator bits indicating whether a message is expected on a full paging channel. An exemplary quick paging channel is given in U.S. Patent No. 6,421,540, issued July 16, 2002 filed February 19, 1999, entitled "A Method And Apparatus For Maximizing Standby Time Using A Quick Paging Channel," which is a continuation-in-part of U.S. Patent No. 6,393,295, issued May 21, 2002, filed July 9, 1997 entitled "Dual Event Slotted Paging", which is a continuation-in-part of U.S. Patent No. 6,111,865, issued August 29, 2000, filed May 30, 1997 also of the same title, all of which are incorporated by reference herein.

Please replace paragraph [0009] on page 4 with the following amended paragraph:

[0009] Characteristics of the full paging channel along with a detailed explanation of the quick paging channel are described in U.S. Patent No. 6,421,540, issued July 16, 2002 filed February 19, 1999, entitled "A Method And Apparatus For Maximizing Standby Time Using A Quick Paging Channel," assigned to the assignee herein, and incorporated by reference herein. Paging schemes disclosed in U.S. Patent No. 6,393,295, issued May 21, 2002, filed July 9, 1997 entitled "Dual Event Slotted Paging," and in U.S. Patent No. 6,111,865, issued August 29, 2000

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~~filed May 30, 1997~~, which are incorporated by reference herein, illustrate the basic implementation of a full paging channel in combination with a quick paging channel to provide terminal paging.